

Sun Microsystems, Inc.

3.73(b) POA Transfer

New Atty: Marlene Penilla

Serial Number	Filing Date	Patent	Reel And Frame	Case No./Art Unit	Client Case Number	Title	Detected Or/Abandoned	Status
				2123-2187				
09/436,464	11/8/1999		010507/0041	SUNMP282/2122	P4373/MG	METHOD AND APPARATUS FOR INSERTING DATA PREFETCH OPERATIONS USING DATA FLOW ANALYSIS		Allowed
08/535,830	3/24/2000		010847/0907	SUNMP294/2122	P4678/MG	MODULE SCHEDULING VIA BINARY SEARCH FOR MINIMUM ACCEPTABLE INITIATION INTERVAL METHOD AND APPARATUS		Pending
09/679,945	10/4/2000		011207/0502	SUNMP301/2122	P5333/MG	USING VALUE EXPRESSION GRAPHS FOR DATA FLOW OPTIMIZATIONS		Pending
08/823,207	3/30/2001		011680/0581	SUNMP303/2122	P5446/MG	METHOD AND APPARATUS FOR SIMULTANEOUS OPTIMIZATION OF CODE TARGETING MULTIPLE MACHINES		Pending
10/027,876	12/21/2001			SUNMP284/2123	P3434/MG	METHOD FOR SHIELDING LOGIC SIGNALS		Pending
09/390,071	9/3/1999		010244/0282	SUNMP269/2124	P3657/MG	QUOTIENT DIGIT SELECTION LOGIC FOR FLOATING POINT DIVISION/SQUARE ROOT		Pending
08/511,218	2/23/2000		010590/0484	SUNMP276/2124	P4008/MG	OPTIMIZED SYSTEM AND METHOD FOR PARALLEL LEADING ONE/ZERO ANTICIPATION		Pending
09/501,549	2/8/2000		010552/0588	SUNMP288/2124	P4508/MG	SYSTEM AND METHOD FOR FINE-GRAINED ANALYSIS OF COMPLEX SOFTWARE		Pending
09/586,657	8/1/2000		010842/0183	SUNMP285/2124	P4891/MG	PARALLEL GREATER THAN ANALYSIS METHOD AND APPARATUS		Pending
08/587,178	8/2/2000		010844/0259	SUNMP296/2124	P4892/MG	METHOD AND APPARATUS FOR COMPUTING PARALLEL LEADING ZERO COUNT WITH OFFSET		Pending
10/072,234	2/8/2002		012941/0774	SUNMP307/2131	P5554/MG	METHOD AND SYSTEM FOR PROVIDING A SECURED WIDE AREA NETWORK ACCESS		Pending
09/513,662	2/25/2000			SUNMP289/2133	P4555/MG	REINSTATE APPARATUS AND METHOD TO RECREATE DATA BACKGROUND FOR TESTING SRAM		Pending
09/590,766	6/8/2000		010857/0606	SUNMP292/2133	P4855/MG	SYSTEM AND METHOD FOR IMPLEMENTING MEMORY TESTING IN A SRAM UNIT		Pending
09/643,895	8/18/2000		011081/0288	SUNMP298/2171	P4914/MG	EXPLICIT HIERARCHICAL REGISTER FILE FOR PROCESSORS		Pending
08/238,903	1/28/2003	6,212,594	010131/0752	SUNMP280/2181	P3089/MG	INTERRUPT PERIODS	4/3/2001	Issued
09/204,737	12/21/1998	6,253,332	9821/0013	SUNMP261/2182	P3090/MG	APPARATUS FOR GENERATING SHIFTED DOWN CLOCK SIGNALS	6/26/2001	Issued

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APPENDIX A
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09/087 608	10/12/2000	6,334,180		SUNMP250C/2183	P2258CNT1/MG	PROCESSOR COUPLED BY VISIBLE REGISTER SET TO MODULAR COPROCESSOR INCLUDING INTEGRATED MULTIMEDIA UNIT	12/25/2001	Issued
09/042 075	8/18/2000		011014/0360	SUNMP299/2183	P4913/MG	EXPLICITLY CLUSTERED REGISTER FILE AND EXECUTION UNIT ARCHITECTURE		Pending
09/089 626	8/11/1998	6,263,399	9403/0348	SUNMP257/2187	P2858/MG	MICROPROCESSOR TO NAND FLASH INTERFACE ACTIVE ADDRESS CONTENT ADDRESSABLE	7/17/2001	Issued
09/584 202	5/3/2000		010789/0767	SUNMP287/2187	P4509/MG	MEMORY		Pending

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